

Calculator-Based Synthesis Routine Speeds Microwave Amplifier Design

D.J. Mellor and L.G. Pierson. "Calculator-Based Synthesis Routine Speeds Microwave Amplifier Design." 1977 MTT-S International Microwave Symposium Digest 77.1 (1977 [MWSYM]): 130-133.

A calculator-based synthesis routine provides rapid and accurate design of interstage networks of prescribed gain, bandwidth, ripple and slope for use in microwave amplifiers. The complete design and measurement of a prototype 6-12 GHz amplifier illustrates the computer-aided synthesis approach to microwave amplifier design.

 [Return to main document.](#)